# VTX870

7U VPX Benchtop Chassis, Six 3U Slots with RTM Support



### Key Features

- Open VPX benchtop development platform
- Dedicated Switch/management slot
- Up to five 3U VPX payload slots
- Compatible with 0.8 Inch, 0.85 Inch and 1.0 Inch modules
- Support for Rear Transition Modules (RTMs)
- Redundant cooling in push/pull bottom-to-top airflow configuration
- Front panel system health display
- Optional JTAG Switch Module (JSM)
- Removable side panels for ease of board probing

### Benefits

- 800 W AC Power Input or 650 W DC input
- Electrical, mechanical, software, and system-level expertise in house
- Full system supply from industry leader
- AS9100 and ISO9001 certified company





# VTX870

The VTX870 is a VPX chassis with six 3U VPX slots. The chassis can accept 0.8 Inch, 0.85 Inch and 1.0 Inch pitch modules. The chassis is ideal for commercial deployment. The side panels on both the front and rear slots are removable for ease of probing and debugging a module.

### **Power Supplies**

The VTX870 has a single AC input power supplies to provide 800 W or 650 W DC input. The chassis supplies 95 W/slot and the AC input is universal.

### **Cooling and Temperature Sensors**

The VTX870 provides cooling to the VPX slots designed to meet ANSI/VITA 65 providing 18 CFM per slot at 0.24 in-H2O @ 5000 feet. The VTX870 provides push/pull cooling to the RTM slots.

### Backplane

The backplane provides five 3U VPX payload slots in a star configuration, fully compliant to VITA 46.0 baseline specification with additional support to the RTMs, compliant to VITA 46.10 and OpenVPX VITA 65.

### JSM

There is an optional JTAG Switch Module to provide JTAG access to the front.



Figure 1: VTX870 Chassis Front View



Figure 2: VTX870 Chassis Rear View

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### **Backplane Connections**

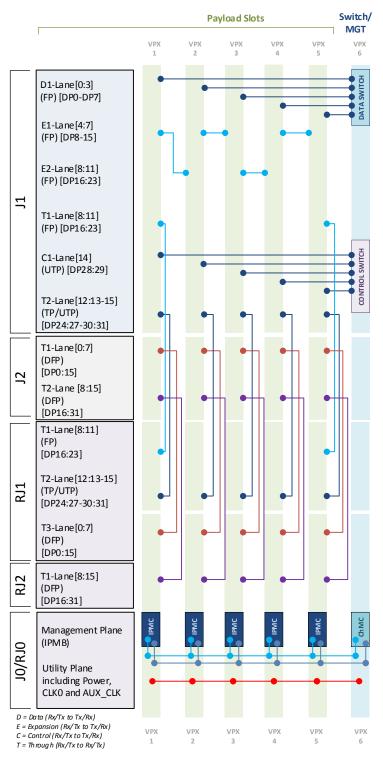


Figure 3: VTX870 Backplane Connections

The initial offering on VTX870 is based on backplane profile BKP3-CEN06\_15.2.2-N. VadaTech can also design additional VITA standard backplane profiles for customer specific applications. Please contact your local sales team for more information.

VTX870 – 7U Open VPX Benchtop Chassis with Six 3U Slots

## Chassis Layout





Figure 4: VTX870 Chassis Layout - Front

Figure 5: VTX870 Chassis Layout - Rear

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# Specifications

Architecture			
Physical	Dimensions	Height: 7U	
		Width: 8.45"	
		Depth: 12.5"	
		Weight: TBD lbs	
Туре	VPX Shelf	5 Payload Slots up to 1.0" pitch with a dedicated Switch/management slot	
Standards			
VPX	Туре	VITA 46.0 Baseline Specification	
Configuration			
Power	VTX870	800 W AC input or -48V DC	
Environmental		See Ordering Options	
Cooling		Bottom to Top	
Other			
MTBF	MIL Hand book 217-F@ TBD hrs		
Certifications	Designed to meet FCC, CE and UL certifications, where applicable		
Standards	VadaTech is certified to both the ISO9001:2000 and AS9100B:2004 standards		
Warranty	Two (2) years		

### INTEGRATION SERVICES AND APPLICATION-READY PLATFORMS

VadaTech has a full ecosystem of OpenVPX, ATCA and MTCA products including chassis platforms, shelf managers, AMC modules, Switch and Payload Boards, Rear Transition Modules (RTMs), Power Modules, and more. The company also offers integration services as well as preconfigured Application-Ready Platforms. Please contact VadaTech Sales for more information.

# Ordering Options

### VTX870 - A00-D00-0HJ

A = Power supply	D = JSM	
0 = 800 W (AC) 1 = 650 W (-48V DC)	0 = No JSM 1 = JSM	
		H = Environmental
		See Environmental Specification Table below
		J = Conformal Coating
		0 = No coating 1 = Humiseal 1A33 polyurethane 2 = Humiseal 1B31 acrylic

### **Environmental Specification\***

Option H	H = 0	
Operating Temperature	AC1* (-5°C to +55°C)	
Storage Temperature	C1* (-40°C to +85°C)	
Operating Vibration	V2* (0.04 g2/Hz max)	
Storage Vibration	OS1* (20g)	
Humidity	95% non-condensing	

Notes: \*Please contact VadaTech Sales for other specification

### **Related Products**

### VPX518



- AMC FPGA carrier for FMC per VITA 57
- Xilinx Zynq-7000 FPGA in FFG-900 package(XC7Z100 or XC7Z045) with embedded ARM®

• 3U FPGA carrier for FPGA Mezzanine Card (FMC) per VITA 46 and VITA 57

• Supported by DAQ Series<sup>™</sup> data

#### VPX592



VPX599



• 3U FPGA Dual DAC and dual ADC per VITA 46

Xilinx Kintex UltraScale<sup>™</sup> XCKU115 FPGA

• High-performance clock jitter cleaner

- Xilinx Kintex UltraScale™ XCKU115 FPGA
- Dual ADC @ 6.4 GSPS 12-bits



# Contact

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