

### KEY FEATURES

- Compact PCI (cPCI) compliant
- Managed Layer two switch
- 24 ports (double slot)
- 12 ports of 10/100/1000 via RJ-45 on the base board
- 12 ports via SFP (daughter module)
- Support up to 8K MAC address
- 4K IEEE 802.1Q VLANs
- VLAN-based packet filtering
- Packet classification using IEEE802.1p QoS
- 9K Jumbo frames
- Spanning tree
- Mirroring
- QoS
- SNMP and RMON
- OS support for:
  - OS independent

The CP218 is a 6U single slot Compact PCI (cPCI) module that has 12 ports of 10/100/1000 GbE via RJ-45 on its base board. The Module come with an optional daughter board which has an additional 12 ports of GbE via SFP which can provide Fiber or Copper interface.

The switch is managed via http and supports a rich set of features such as VLAN, Spanning tree, QoS, Mirroring, etc.

VadaTech can modify this product to meet special customer requirements without NRE (minimum order placement is required).

# 24 Port cPCI Managed Layer Two Switch

## SPECIFICATIONS

Architecture		
Physical	Dimensions	6U
		6.366 x 9.187 (PCB size)
		Single slot on the base board and double slot with the daughter card
Type	cPCI Ethernet	GbE switch
		24 ports
		10/100/1000 per port
Standards		
CompactPCI	Type	cPCI
Configuration		
Power	CP218	6W Base module
Environmental	Temperature	Operating Temperature: 0° to 65° C (Air flow requirement is to be greater than 200 LFM)
		Storage Temperature: -40° to +90° C
	Vibration	1G, 5-500Hz each axis
	Shock	30Gs each axis
Front Panel	Relative Humidity	5 to 95 percent, non-condensing
	Interface Connectors	RJ-45 on the base board and SFP on the daughter module
		LEDs
	Mechanical	Hot Swap Ejector Handle
Software Support	Operating Systems	Independent
Other		
MTBF	MIL Handbook 217-F > TBD Hrs.	
Certifications	Designed to meet FCC, CE and UL certifications where applicable	
Standards	VadaTech is certified to both the ISO9001:2000 and AS9100B:2004 standards	
Compliance	RoHS and NEBS	
Warranty	Two (2) years.	
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# 24 Port cPCI Managed Layer Two Switch

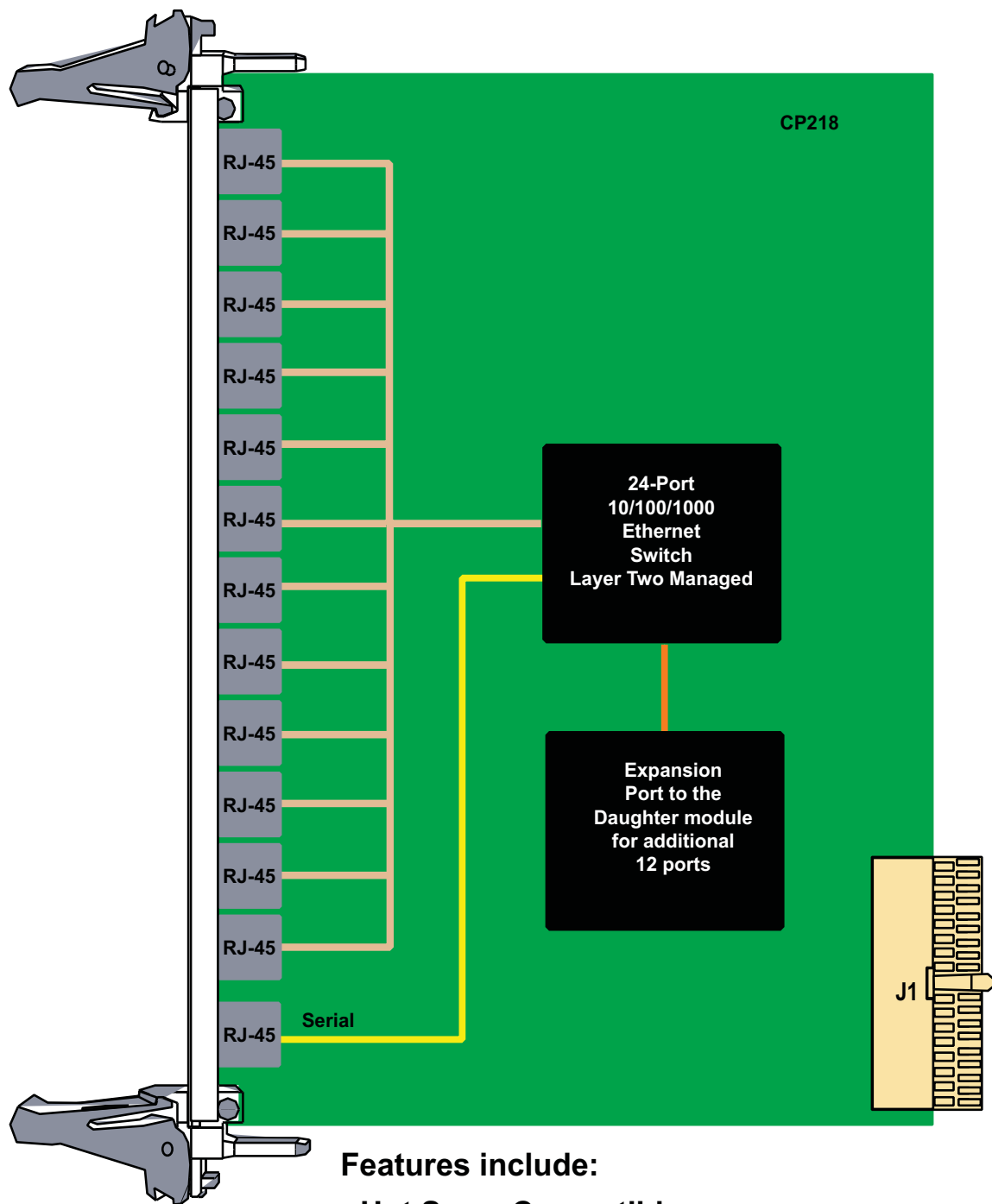


FIGURE 1. CP218 Functional Block Diagram

## Managed Layer Two GbE

The GbE layer two managed switch fabric routes six GbE to front panel and two to the rear.

### Key features:

- ❖ Configuration
  - ◆ Ethernet/IEEE 802.3 Packet size (64 bytes to 1522 bytes)
  - ◆ Jumbo packets up to 9216 bytes
- ❖ L2 Switching
  - ◆ Supports up to 8K MAC address
  - ◆ Line rate switching for all packet sizes
  - ◆ Independent VLAN learning
  - ◆ VLAN flooding for broadcast and DLF packets
  - ◆ Hardware-based address learning
  - ◆ Six CPU-managed learning (CML) modes per port
  - ◆ Hardware-and-software-based aging
  - ◆ Software insertion/deletion/lookups of the L2 table
  - ◆ Same port bridging supported
  - ◆ Station movement control
- ❖ L2 Multicast
  - ◆ 4K VLANs
  - ◆ Protocol-based VLANs
  - ◆ IEEE 802.1p
  - ◆ IEEE 802.1Q
  - ◆ Independent VLAN learning (IVL)
  - ◆ Ingress filtering for IEEE 802.1Q VLAN security
  - ◆ VLAN-based packet filtering
  - ◆ MAC-based VLAN
- ❖ Source Port Filtering
  - ◆ Egress port block masks
  - ◆ Trunk group blocking masks
- ❖ Storm Control Per-Port:
  - ◆ Unknown unicast packet rate control
  - ◆ Broadcast packet rate control
  - ◆ Multicast packet rate control
- ❖ Spanning Tree:
  - ◆ IEEE 802.1D spanning tree protocol (single spanning tree per port)
  - ◆ IEEE 802.1s for multi spanning trees
  - ◆ IEEE 802.1w rapid spanning tree protocol-delete and/or replace per:
    - Port
    - VLAN
    - Port, per VLAN
  - ◆ Spanning tree protocol packets detected and sent to the CPU
- ❖ Double-Tagging:
  - ◆ Unqualified learning/forwarding
  - ◆ IEEE 802.1 Q-in-Q
- ❖ Mirroring
  - ◆ Ingress/egress mirroring support
  - ◆ Mirror-to-port receives the unmodified packet for ingress mirroring
  - ◆ Mirror-to-port receives the modified packet for egress mirroring
- ❖ Content Aware Filter Processing
  - ◆ Intelligent Protocol Aware processor with backward-compatible, byte-based classification option
  - ◆ Parses up to 128 bytes per packet
  - ◆ -512 ACL rules support
  - ◆ Multiple matches and actions per packet
  - ◆ ACL-based policing
  - ◆ Ingress/egress port based filtering
  - ◆ MAC destination address remarking
  - ◆ Traffic class definition based on the filter
  - ◆ Programmable meters allows policing of flows
  - ◆ Metering granularity from 64 Kbps to 1Gbps
  - ◆ Multiple look-ups per packet
  - ◆ Metering support on ingress ports and CPU queues
- ❖ QoS Features
  - ◆ Four CoS queues per port
  - ◆ Per-port, per CoS drop profiles
  - ◆ Port level shaping
  - ◆ Traffic shaping available on CPU queues
  - ◆ Programmable priority to CoS queue mapping
  - ◆ Provides two levels of drop precedence per queue
  - ◆ Strict Priority (SP), Weighted Round Robin (WRR), and Deficit round Robin (DRR) mechanisms for shaped queue selection
- ❖ DSCP
  - ◆ DSCP-based prioritization
  - ◆ Back pressure metering
  - ◆ DSCP to IEEE 802.1p mapping
- ❖ Port Security
  - ◆ Per port blocking
  - ◆ Supports IEEE 802.1x
  - ◆ MAC address blocking
- ❖ DoS Prevention
  - ◆ Denial of Service detection/prevention
- ❖ Management Information Base
  - ◆ SMON MIB, IETF RFC 2613
  - ◆ RMON statistics group, IETF RFC 2819
  - ◆ SNMP interface group, IETF RFC 1213, 2836
  - ◆ Ethernet-like MIB, IETF RFC 1643
  - ◆ Ethernet MIB, IEEE 802.3u
  - ◆ Bridge MIB, IETF RFC 1493

# 24 Port cPCI Managed Layer Two Switch

## ORDERING OPTIONS

CP218 - AAB - BCC - OHJ

**AA = Number of Fiber SX Transceivers**

- 0 = None
- X = Number of Transceivers

**BB = Number of Fiber LX Transceivers**

- 0 = None
- X = Number of Transceivers

**CC = Number of Fiber LX Transceivers**

- 0 = None
- X = Number of Transceivers

**H = Operating Temp**

- 0 = Commercial
- 1 = Industrial

**J = Conformal Coating**

- 0 = None
- 1 = Humiseal 1A33 Polyurethane
- 2 = Humiseal 1B31 Acrylic

\*Total number of transceivers must not exceed 12.



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