

PRODUCT DATA SHEET

SLE SINGLE BOARD COMPUTER



Featuring dual Intel® Pentium® III processors and sophisticated SMP for multi-threaded applications and enhanced throughput, the SLE handles your current needs and tough applications. The SLE also features SDRAM memory, giving you a cost-effective, proven technology. Additionally, the SLE has dual Ethernet interfaces and Ultra3 SCSI. The SLE supports a 133MHz Front Side Bus (FSB) and a 64-bit/66MHz PCI Local Bus. With all of these features on board, you have more available slots to handle today's requirements and build for the future.

PROCESSOR:

Intel[®] Pentium[®] III processors - FC-PGA/FC-PGA2 Speeds: 733MHz - 1.26GHz

CHIPSET:

ServerWorks ServerSet[™] III LE chipset supports a Front Side Bus bandwidth of 133MHz, with full support for ECC on the memory bus. Supports single-bit error correction, with multiple-bit error detection.

CACHE MEMORY (L2):

The Pentium III processor includes an integrated on-die, 256K or 512K 8-way set associative level two (L2) cache with a 256-bit wide bus. The processor also includes a 16K level one (L1) instruction cache and 16K L1 data cache. These cache arrays run at the full speed of the processor core.

FRONT SIDE BUS:

The ServerWorks ServerSet III LE supports speeds of 133MHz. The Front Side Bus provides a path for transferring data between main memory/chipset and the microprocessors.

PCI ULTRA3 SCSI INTERFACE:

The Ultra3 SCSI interface is a PCI Bus Master device using the QLogic ISP10160A SCSI controller, which supports Ultra3 SCSI maximum throughput of 160MB per second. Ultra160 drives are supported. Enable/disable active termination is provided with terminator voltage protected by self-resetting fuses. Software drivers are available for most popular operating systems.

PCI 10/100BASE-T ETHERNET INTERFACES (DUAL):

The dual PCI Ethernet interfaces (Intel 82559) support 10/100Base-T via RJ-45 connectors on the board's I/O bracket. The interfaces are compliant with IEEE 802.3 and PCI Local Bus 2.1 Specifications. Link status and activity LEDs are on the I/O bracket. Software drivers are available for most popular operating systems.

PCI EIDE ULTRA DMA/33 INTERFACES (DUAL):

Dual high performance PCI Bus Master EIDE interfaces are capable of supporting two IDE disk drives each in a master/slave configuration. Supports Ultra DMA/33 with synchronous DMA mode transfers up to 33MB per second.

DRAM MEMORY:

The DRAM interface consists of two dual in-line memory module (DIMM) sockets and supports auto detection of memory up to 2GB of Synchronous DRAM (SDRAM) memory. The DIMM memory is PC-133 compliant, which means that it complies with IBM's PC133 SDRAM Registered DIMM Design specification. Uses industry standard 72-bit wide (ECC) gold finger DIMM modules in two 168-pin DIMM sockets.

BUS SPEEDS:

ISA	- 16-bit/8MHz
PCI	- 32-bit/33MHz
	- 64-bit/33MHz
	- 64-bit/66MHz
System/FSB	- 133MHz

BIOS (FLASH):

AMIBIOS 7.0 with built-in advanced CMOS setup for system parameters, peripheral management for configuring on-board peripherals, PCI-to-PCI bridge support, and PCI interrupt steering. Supports flash devices for BIOS upgrading via floppy interface. Custom BIOSs available.

PCI LOCAL BUS INTERFACE:

The SLE provides on-board Primary and Secondary PCI Bus interfaces. Both the Primary and Secondary PCI Bus interfaces provide a sixteen deep I/O cache and a four deep request queue for PCI to Memory cycles.

Primary PCI Interface: The Primary PCI interface is 32 bits wide, runs at 33MHz and supports on-board SCSI, video and dual Ethernet interfaces.

Secondary PCI Interface: The Secondary PCI interface is 64 bits wide and runs at 33/66MHz. The interface is routed off board to drive PCMIG[®] compliant PCI/ISA backplanes.

SYMMETRIC MULTIPROCESSING (SMP):

The dual Pentium III processor-based design allows the operating system to assign tasks on demand to the next available processor. SMP uses applications which are divided into threads which can run concurrently on any available processor.

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Dependable, always.



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SUPER VGA INTERFACE:

The Intel 69030 video interface has 4MB of on-chip memory and supports up to 1280 x 1024 pixel resolutions. Software drivers are available for most popular operating systems.

ADDITIONAL SLE FEATURES:

System Hardware Monitor:

• Provides monitoring of system voltages, temperature and fan speed.

Watchdog Timer:

• The software controlled watchdog timer monitors system activity and generates a reset pulse in the event of a timeout.

I/O Features:

- Two high-speed serial ports
- Enhanced bi-directional parallel interface
- Dual Universal Serial Bus (USB, Rev. 1.1)
- PS/2 mouse/keyboard interface
- Floppy drive interface

MEAN TIME BETWEEN FAILURES (MTBF):

148,000 POH (Power-On-Hours) at 40° C., per Bellcore

SLE APPLICATION CONSIDERATIONS:

- Power Requirements: +5V Typical 14.95 Amps 1.26GHz
 - 14.95 Amps
 1.266Hz

 14.02 Amps
 1.0GHz Rev. L-07 and later

 13.6 Amps
 1.0GHz Rev. K-06 and earlier

 11.4 Amps
 866MHz

 10.5 Amps
 733MHz
 - +12V @ 500 mAmps for Rev. L-07 and later
 - +12V @ 600 mAmps for Rev. K-06 and earlier -12V @ <100 mAmps

Temperature/Environment: Operating Temperature: 0° to 60° C. for 866MHz and below 0° to 50° C. for 933MHz 0° to 50° C. for 933MHz 0° to 50° C. for 1.0GHz 0° to 50° C. for 1.26GHz Storage Temperature: -40° to 70° C. Humidity: 5% to 90% non-condensing

Mechanical:

A low-profile (1.44" height) active cooling system is used on the SLE to insure reliable processor operation at elevated temperatures. Overall dimensions of the SLE, including the active cooling system, are 13.3" L (338mm) x 4.8" H (121.9mm) x 1.57" W (39.9mm).

STANDARDS:

- IEEE P996, Personal Computer Bus Standard
- PCI Local Bus Specification 2.1
- PICMG[®] 1.0 Specification

AGENCY APPROVALS:

 Designed for UL1950, CAN/CSA C22.22 No. 950-95, EN610000-6-2: 1999; EN55022: 1998, Class B EN61000-3-2: 1995/A2; 1998; EN6100-3-3; 1995

ORDERING INFORMATION:

Model Name: SLE		
Model #	CPU Speed	
5891-602-xM 5891-407-xM 5891-405-xM 5891-403-xM	1.26GHz 1.0GHz 866MHz 733MHz	

⁽xM = Memory)

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