The TPMC683 is a standard single-width 32 bit PMC module offering 32 differential I/O lines using EIA-422 / EIA-485 compatible, ESD-protected line transceivers.

Each line is individually programmable as input or output. The receivers are always enabled, which allows determining the state of each I/O line at any time. This can be used as read back function for lines configured as outputs.

Each input can generate an interrupt. Signal edge handling is programmable to interrupt on rising and/or falling edge of an input signal. Interrupts can be enabled and disabled for each bit. For interrupt source detection, the status of each line can be read from interrupt status registers.

The TPMC683 provides front panel I/O via a HD68 SCSI-3 type connector and rear panel I/O via P14.

For First-Time-Buyers the engineering documentation TPMC683-ED is recommended. The engineering documentation includes TPMC683-DOC, schematics and data sheets of TPMC683.

Order Information

RoHS Compliant
TPMC683-10R 32 RS422/RS485 Differential I/O

Documentation
TPMC683-DOC User Manual
TPMC683-ED Engineering documentation
(Allegro Drawings, Schematics, Assembly Drawing, Data Sheets)

Software
TDRV012-SW-25 Integrity Software Support
TDRV012-SW-42 VxWorks Software Support
(Temporary and VxBus-Enabled Software Support)
TDRV012-SW-65 Windows XP/XPE/2000 Software Support
TDRV012-SW-72 LynxOS Software Support
TDRV012-SW-82 Linux Software Support
TDRV012-SW-95 QNX 6 Software Support

For other operating systems please contact TEWS.

Related Products
TA304 Cable Kit for modules with HD68 SCSI-3 type connector
TPIM003 PIM I/O Module with HD68 SCSI-3 type connector and special pin assignment.

TEWS TECHNOLOGIES GmbH keeps the right to change technical specification without further notice.

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