TPMC671  16 Digital Inputs (24V) 16 Digital Outputs (24V, 0.5A)

Application Information
The TPMC671 is a standard single-width 32 bit PMC with 16 24V digital inputs galvanically isolated from the computer system by optocoupler. The inputs are also potential free to each other. A high performance input circuit ensures a defined switching point and polarization protection against confusing the pole. All inputs have a common electronic debounce circuit with a freely programmable debounce time. All inputs can generate an interrupt. The signal edge handling is programmable to interrupt on rising, falling or both edges of the input signal.

The TPMC671 has 16 digital high side or low side switches (build option) with galvanic isolation from the computer system by optocoupler. The outputs are also isolated against each other in groups of four outputs. All outputs are protected against short-circuit and thermal overload. The output drivers are capable of driving 0.5A continuous per channel. A hardware watchdog clears all outputs in case of trigger failure. The TPMC671-1x provides front panel I/O, the TPMC671-2x provides P14 I/O.

For First Time Users the Engineering Documentation TPMC671-ED is recommended. The Engineering Documentation includes TPMC671-DOC, schematics and data sheets. Software Support (TPMC671-SW-xx) for different operating systems is available.

Technical Information
- Standard single-width 32 bit PMC module conforming to IEEE P1386.1
- PCI 2.1 compliant interface
- 3.3V and 5V PCI Signaling Voltage
- Board size: 149 mm x 74 mm
- 16 interrupt generating digital inputs
- 24V signal voltage for inputs
- Optocoupler for galvanic isolation
- All inputs isolated to each other
- Programmable electronic debounce circuit (7µs to 440ms in steps of 7µs),
- 16 digital outputs, high side or low side switches (build option)
- 24V signal voltage, current per output 0.5A
- Optocoupler for galvanic isolation
- Outputs are short-circuit protected
- Outputs are isolated to each other in groups of four
- Outputs are protected against thermal overload
- Watchdog timer resets all channels in case of trigger failure
## Order Information

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### Documentation

- **TPMC671-DOC**: User Manual
- **TPMC671-ED**: Engineering Documentation, includes TPMC671-DOC

### Software

- **TDRV003-SW-25**: Integrity Software Support
- **TDRV003-SW-42**: VxWorks Software Support (Legacy and VxBus-Enabled Software Support)
- **TDRV003-SW-65**: Windows XP/XPE/2000 Software Support
- **TDRV003-SW-72**: LynxOS Software Support
- **TDRV003-SW-82**: LINUX Software Support
- **TDRV003-SW-95**: QNX 6 Software Support

For other operating systems please contact TEWS.

### Related Products

- **TA304**: Cable Kit for modules with HD68 Connector
- **TPIM002**: PIM I/O Module with HD68 SCSI-2 type connector