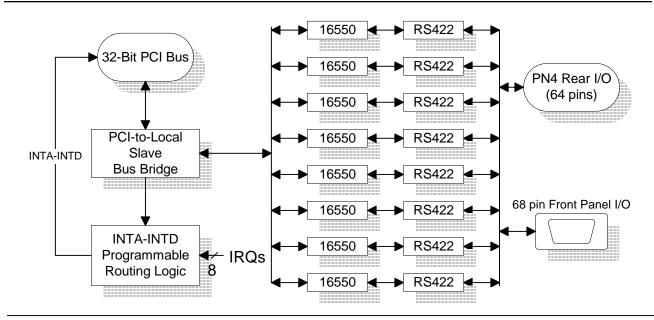
## 8-Port RS422 Async Communication Adapter



The Technobox 8-Port Async Communication adapter provides eight 16550-based UARTs using RS422 signaling levels.

Each port provides two differential inputs, and two differential outputs, as follows:

RTS+ (Request to Send Positive)

RTS- (Request to Send Negative)

CTS+ (Clear to Send Positive)

CTS- (Clear to Send Negative)

TXD+ (Transmit Data Positive)

TXD- (Transmit Data Negative)

RXD+ (Receive Data Positive)

RXD- (Receive Data Negative)

Each differential pair is terminated by a 150 OHM parallel resistor located on the PMC card.

All eight ports are available out the PMC front panel via a 68-pin SCSI-style connector. Also, the ports are wired to the P4 connector at the rear of the PMC, allowing connection of all eight ports to the VMEbus P2 connector for host processors supporting rear-I/O connection.

A transition panel available from Technobox converts the 68-pin SCSI style connector into eight 9-pin male D-Subminiature connectors for convenient access to the individual ports.

For Rear I/O, a paddle card available from Technobox converts the P2 'a' and 'c' rows back into the 68-pin SCSI style connector. This permits use of the eight 9-pin D-Sub transition panel for rear-exit from the card cage.

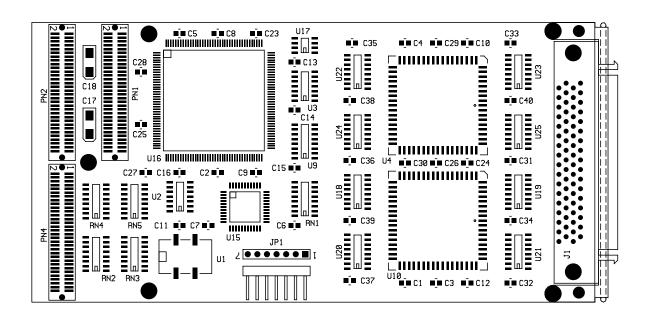
The transition panel may also be mounted at any convenient point in a user's system. For more information, refer to the transition panel products found at the back of this catalog.

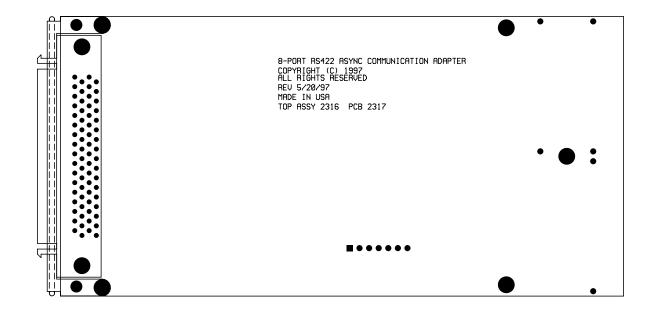
The UARTs operate at standard baud rates from 300 baud to 115K-baud. Other odd-baud rates are possible and are available on special order by substituting the Baud Rate oscillator crystal on the PMC.

The eight interrupt requests from each UART are routed to one of the four INTx lines on the PCI bus. This design avoids interrupt routing switches by incorporating a programmable element which can be set up from the host processor, allowing any UART to be routed to any INTx request line. The default after reset is all UART interrupt requests are routed to the INTA line.

The 16550 register set is accessed from the host processor using Programmed-I/O. "C" source code is provided with the board to illustrate how to set up the PCI bus bridge in order to access the UART registers. Once the registers are mapped into host I/O space, standard async communication drivers may be used to access the ports.

## 8-Port RS422 Async Communication Adapter





## **Product Summary**

Technobox Part Number: 2316

Typical Power Dissipation: TBD watts

Power Supplies Required: +5

PCI Signaling Environment: 5 Volt