

ICS-1572

2-Channel, 400 MHz 14-bit ADC, 2-channel, 500 MHz 16-bit DAC PMC Module with Virtex-5 SX95T User Programmable FPGA

Features:

- 2 Transformer-coupled Analog Inputs, 14 bits, $F_s \leq 400$ MHz per channel
- 2 Transformer-coupled Analog Outputs, 16 bits, $F_s \leq 500$ MHz per channel
- Xilinx Virtex-5 SX95T User Programmable FPGA
- PCI-X 64-bit/133 MHz Master/Target Burst Mode DMA capable
- 64 User I/Os via Pn4 connector routed directly to FPGA
- Pn4 LVDS or LVTTTL signal levels
- VxWorks, Linux and Windows software drivers

The ICS-1572 builds on the legacy of our industry-leading digital receiver families to bring a new transceiver to market. Designed for communications, radar and test & measurement applications, it provides unsurpassed converter technology with industry-leading DSP expertise. The result is a cost-effective combination of size and performance in a single PMC site.

The ICS-1572 provides two 14-bit ADCs sampling synchronously at frequencies up to 400 MHz and two 16-bit DACs at up to 500 MHz. A Xilinx Virtex-5 SX95T FPGA is provided for user-defined signal processing functions, giving greater capacity than previous generations.

A Graychip GC5016 digital down-converter (DDC) provides simultaneous down- and up-conversion of up to four arbitrary signal bands. It provides programmable decimation of 1 to 4K. Each band can be tuned to any arbitrary centre frequency within the pass band. Output data can be in either real or complex format, providing a maximum bandwidth of 160 MHz (-1dB) on each channel.

The user FPGA is not used for board control functions, thus providing maximum occupancy for customer applications. It provides a powerful signal processing capability that can be loaded with standard functions such as wideband DDC, FFT and time stamping, or programmed by the user for any required function.

The product provides internal and external clock and trigger capability, and supports multiple board synchronous sampling both of converter and DDC functionality.

A Hardware Development Kit (HDK) provides support for users who wish to implement their own signal processing algorithms in the FPGA. Alternatively, our FPGA applications programming team can develop FPGA cores specific to customer needs.

A standard programmable wide band DDC core is provided with the HDK that may either be used by itself or to provide sample rate reduction for input to the GC5016 DDC.

64 User I/Os connected directly from the FPGA to the Pn4 connector provide an alternative high-speed data path to the module, supporting LVTTTL or LVDS signal levels (factory-configured).

Memory resources consist of 16 MBytes of QDR II SRAM. The standard memory design implements four FIFOs, but the memory may be used in different ways, or for scratchpad, if required.

The ICS-1572 can be used with any type of carrier card that will accept a PMC module, including VME, PCI and CompactPCI. When used with an appropriate DSP/processor carrier card, the ICS-1572 offers a powerful single slot solution for software radio applications.

Software Development Kits (SDK) are available for VxWorks, Linux and Windows operating systems. Each SDK includes operating examples in 'C'.

The product is ideally suited for demanding applications in military communications, 3G and 4G cellular base station development, signal intelligence, smart antenna, radar beam forming, wireless test & measurement and satellite ground stations.

ICS-1572 2-Channel 400 MHz 14-bit ADC, 2-Channel 500 MHz 16-bit DAC PMC Module with Xilinx Virtex-5 SX95T User-programmable FPGA

Specifications

Analog Input

- Two analog inputs, 14-bit, $F_s \leq 400$ MHz, transformer-coupled, SSMC connectors
- Two analog outputs, 16-bit, $F_s \leq 500$ MHz, transformer-coupled, SSMC connectors
- 50 Ohm input/output impedance
- Full scale input voltage 4.8 dBm (1.1 Vpp)
- Input signal bandwidth of 4.5 to 1400 MHz (-3 dB point)
- Minimum input sample rate of 20 MHz/channel
- Full scale output voltage 4.8 dBm (1.1 Vpp)
- Input signal bandwidth of 4.5 to 220 MHz (-3 dB point)
- Minimum output conversion rate of 500 MHz/channel
- Internal sample clock oscillator 400 MHz (programmable)
- Sampling on rising edge of internal or external sample clock
- External trigger LVTTTL/LVCMOS 5V tolerance, software selectable Rising/Falling edge
- External sync. LVTTTL/LVCMOS 5 V tolerance, software selectable Rising/Falling edge
- External clock LVTTTL/Sinewave compatible, -3 dB min. ~ +6 dBm max.
- $S/(N+D) > 62$ dBFs @ $f_{in} = 200$ MHz @ 400 MSPS, typ.
- SFDR 75 dBc @ $f_{in} = 350$ MHz, typ.

General

- IEEE std. 1386.1-2001 PMC compatible
- VxWorks, Linux and Windows software drivers

Onboard Resources

- Xilinx Virtex-5 SX95T user programmable FPGA
- 16 MBytes of QDR II SRAM in two banks
- SSMC connectors

I/O Specifications

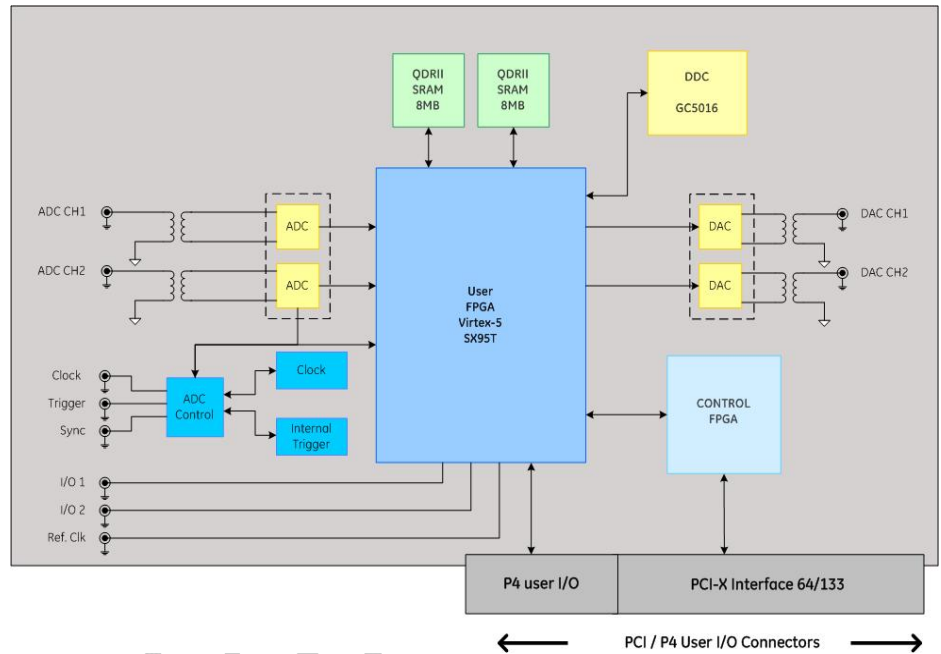
- PCI-X 64-bit 133 MHz Master/Target Burst Mode DMA capable
- All 64 user programmable I/O via Pn4 connector routed directly to FPGA
- Pn4 user definable LVDS or LVTTTL signal levels (factory termination option)

Environmental

- Operating temperature, 0°C to +55°C
- Storage temperature, -40°C to +85°C
- 95% non-condensing humidity
- Required cooling 200 LFM

Preliminary Data Sheet – specifications are subject to change

Block Diagram



Ordering Information

- | | |
|---------------|-----------------------|
| ICS-1572A-000 | ICS-1572, ADC and DAC |
| ICS-1572A-001 | ICS-1572, ADC only |
| ICS-1572A-002 | ICS-1572, DAC only |

DRV-1572-VXW

Software Development Kit for VxWorks operating system

DRV-1572-LX

Software Development Kit for Linux operating system

DRV-1572-WIN

Software Development Kit for Windows operating system

HDK-1572

Hardware Development Kit for FPGA development by user, including a default core (included with board)

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GE Fanuc Intelligent Platforms is a leading global provider of embedded computing solutions for a wide range of industries and applications. Our comprehensive product offering includes many types of I/O, single board computers, high performance signal processors, fully integrated, rugged systems including flat panel displays, plus high speed networking and communications products. The company is headquartered in the U.S. and has design, manufacturing and support offices throughout the world. Whether you're looking for one of our standard products or a fully custom solution, GE Fanuc Intelligent Platforms has the breadth, experience and 24/7 support to deliver what you need. For more information, visit www.gefanucembedded.com call 1-800-GE Fanuc.



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Additional Resources

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