- MIL-STD-1397C Type A, B, C, and H compliant
- Full-duplex 8, 16, or 32-bit NTDS transfers
- Interrupt, PIO & DMA operation
- Independent NTDS input and output channels
- Field Programmable Gate Array (FPGA) technology
- Separate word counters and time-outs for EI/EF words and data words on inputs and outputs
- · PCI master and slave operation
- Short circuit protected, tri-state drivers
- Internal loop-back test without disconnecting NTDS cables
- Software enabled time-stamp on input words with 125 ns resolution
- Timestamps can be synchronized across multiple interfaces
- Supports receipt of multiple forced EFs

## **Input Mode Features**

- · Separate or combined data and command word buffers
- · Input command words, stop on data word
- · Input data words, stop on command word
- · Passive tap mode

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## **Output Mode Features**

· Concurrent data and command buffer operation

## **Time-out Features**

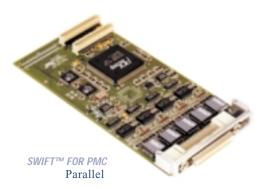
- Time-out values in 10  $\mu s$  or 1 ms increments
- Time-out between words and/or total transfer times
- · Start time-out at beginning of operation or upon transfer of the first word

## **Software Drivers** \*

Windows NT • Vx Works • Solaris • Linux & RT Linux • HP-UX

\*Choice of driver included with board purchase. Check with factory for new OS support

SPECIFICATIONS	
NTDS Interface:	MIL-STD-1397C Type A, B, C, & H PCI
Bus Interface:	PCI 2.1 Compliant (3.3 or 5V I/O, 32 Bit, 33 MHz)
Input Buffer:	4K x 32-Bit FIFO (standard)
	64K x 32-Bit FIFO (optional)
NTDS I/O Connector:	120 Pin Docking Connector (Molex 52755-1200)
Relative Humidity:	0% to 90% (non-condensing)
Operating Temperature:	0°C to +55°C
ORDERING INFORMATION	
MODEL#	PRODUCT DESCRIPTION
SP-01101-00	Swift <sup>TM</sup> NTDS A/B/C/H PMC Module, 4K** FIFO
	** Also available with 64K FIFO



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