VMIVME-2528
128-bit TTL Digital Input/Output Board

- 128 bits of TTL compatible I/O
- Individual port direction control jumpers
- 24 mA output current sink capability
- 8- or 16-bit transfers
- Double Eurocard form factor
- Nonprivileged or supervisory short I/O transfers
- Alternating grounds on I/O pins
- Real time loopback
- Positive or negative true data I/O option

FUNCTIONAL CHARACTERISTICS

Compatibility: VMEbus specification compatible double height form factor

I/O Connector Type: 64-pin DIN 41612

I/O Organization: Sixteen I/O ports, eight bits wide. Addressable to any address within short supervisory or short nonprivileged I/O map. Individual port direction control jumpers are provided.

Addressing Scheme: Sixteen ports individually addressable on 8- or 16-bit boundaries. Twelve DIP switches provide unlimited short data I/O address map selection.

Data Transfer Bus: A16: D16

Powerup Initialization: All outputs are initialized in the tristate mode by master clear. Any write transfer to the final port automatically releases tristate mode.

I/O Circuit: TTL compatible
- Sink - 24 mA
- Source - 6.5 mA

Data Polarity: High-true or low-true

Installation: Any slot except A1

PHYSICAL/ENVIRONMENTAL

Temperature Range: 0 to 55 °C, operating
-20 to 85 °C, storage

Relative Humidity Range: 20 to 80 percent, noncondensing

Cooling: Convection

Power Requirements: +5 V at 3 A maximum

TRADEMARKS

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For Ordering Information, Call:
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E-mail: info@vmic.com Web Address: www.vmic.com
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VMIVME-2528

I/O REGISTERS

ADDRESS COMPARE

ADDRESS SELECTION

CONTROL LOGIC

ADDRESS BUFFER

I/O CONTROL

I/O REGISTERS

VMIVME-2528 Functional Block Diagram