

16TE3Sixteen-channel interface for E3/T3



Description

The 16TE3 is a mezzanine board that pairs with an EDT main board (for PCI or PCI Express) for high-speed data transfer. It supports sixteen T3 or E3 interfaces and processes large amounts of communications serial data for complex, user-defined applications.

EDT provides standard FPGA configuration files, and custom configuration files can be requested.

The main board supplies high-speed DMA, plus additional memory and programmable FPGA resources.

Applications

Communications monitoring (serial data)

Signal generation (E3 or T3)

Features

Mezzanine board – pairs with an EDT main board (in a PCI, PCI-X, or PCIe bus), which adds high-speed DMA, programmable FPGA resources, and memory

E3/T3: Sixteen channels (full duplex line interfaces)

Product Type	16TE3 is a 16-channel mezzanine board for E3/T3; it requires a main board.	
FPGAs and Memory	Programmable FPGA and memory resources are provided by the main board.	
Data Rates	Data rates are dependent on data format and main board.	
Data Format (I/O)	Transformer coupled Compliance (waveforms) Compliance (jitter tolerance) Independent I/Os MB/s Encoding/decoding Jitter attenuation	E3/T3 Yes G.703 ANSI T1.102-1993, Telcordia GR-499-CORE, and G.823 16 (each bidirectional) 75-Ω coaxial cable over 1100 feet at speeds up to 51.84 MB/s B3ZS or HDB3 Select on transmit or receive
Connectors	One 62-pin AMP (mate to AMP 748367-1)	
Cabling	Consult EDT for purchase options.	
Physical	Weight Dimensions	3.7 oz. typical 6.6 x 4.2 x 0.5 in. (with a main board)
Environmental	Temperature Humidity	Operating 0° to 40° C Non-operating -40° to 70° C Operating 1% to 90%, non-condensing at 40° C Non-operating 95%, non-condensing at 45° C
System and Software	For details on system requirements and EDT-provi	ided software driver packages, see specifications for your EDT main board.

Support

EDT offers engineer-to-engineer customer support, from phone consultation to custom design of hardware, firmware, and software. Contact us for options and details.

Contact

Engineering Design Team (EDT), Inc. 1400 NW Compton Drive, Suite 315 Beaverton, Oregon 97006 800-435-4320 / 503-690-1234 (phone) 503-690-1243 (fax) www.edt.com

Ordering Options

- Main board: PCI SS / PCI GS / PCIe8 LX

For more options, see main board datasheet. **Ask about custom options.**