DAQ-2213/2214, DAQe-2213/2214

16-CH 16-Bit 250 kS/s Low-Cost Multi-Function DAQ Cards

Features

- Supports a 32-bit 3.3 V or 5 V PCI bus (DAQ-2213, DAQ-2214)
- x1 lane PCI Express® Interface (DAQe-2213, DAQe-2214)
- Onboard 1 k-sample A/D FIFO
- Bipolar or unipolar analog input ranges
- Programmable gains: x1, x2, x4, x8
- 512-configuration channel gain queue
- Scatter-gather DMA

 2-CH 12-bit multiplying analog outputs with waveform generation (DAQ/DAQe-2214)
- Onboard 1 k-sample D/A FIFO (DAQ-2214, DAQe-2214)
- 24-CH TTL digital input/output
- 2-CH 16-bit general-purpose timer/counter
- Analog and digital triggering
- Fully auto calibration
- Multiple cards synchronization through SSI (System Synchronization Interface) bus
- Operating Systems
 Windows Vista/XP/2000/2003

achieve maximum noise elimination.

- I inux
- Recommended Software
- VB.NET/VC.NET/VB/VC++/BCB/Delphi

ADLINK DAQ-2213/2214 and DAQe-2213/2214 can sample up to 16 Al channels with different gain settings and scan sequences. It makes them ideal for dealing with analog signals with various input ranges and sampling speeds. These devices also offer differential mode for 8 Al channels in order to

In addition to the analog input functions, DAQ/DAQe-2214 features 2-CH 12-bit analog outputs. The analog outputs are capable of waveform generation. The DAQ-2213/2214 and DAQe-2213/2214 also feature analog and digital triggering, 24-CH programmable digital I/O lines and 2-CH 16-bit general-purpose timer/counter.

Like all the other members in DAQ-2000 and DAQe-2000 family, multiple DAQ/DAQe-2213 and DAQ/DAQe-2214 can be synchronized through the SSI (system synchronization interface) bus. The auto-calibration functions adjust the gain and offset to within specified accuracies such that you do not have to adjust trimpots to calibrate the cards.

DAQBench

Introduction

■ Driver Support

- DAQPilot for Windows
- DAQ-LVIEW PnP for LabVIEW™
- DAQ-MTLB for MATLAB[®]
- D2K-DASK for Windows
- D2K-DASK/X for Linux

DAQe-2213/2214



DAQ-2213

Pin Assignment

Connector CN2

NC / DAUOU1*	1	35	AOGND* / NC
NC / DA1OUT*	2	36	AOGND* / NC
NC / AOEXTREF*	3	37	AOGND* / NC
NC	4	38	NC
DGND	5	39	DGND
RESERVED / EXTWFTRIG*	6	40	DGND
EXTDTRIG	7	41	DGND
SSHOUT	8	42	DGND
RESERVED	9	43	DGND
RESERVED	10	44	DGND
RESERVED / AFI1*	11		DGND
AFI0	12	46	DGND
GPTC0_SRC	13	47	DGND
GPTC0_GATE	14	48	DGND
GPTC0_UPDOWN	15		DGND
GPTC0_OUT	16		DGND
GPTC1_SRC	17		DGND
GPTC1_GATE	18		DGND
GPTC1_UPDOWN	19		DGND
GPTC1_OUT		54	DGND
EXTTIMEBASE	21		DGND
PB7	22		PB6
PB5	23		PB4
PB3	24	58	PB2
PB1	25		PB0
PC7	26		PC6
PC5	27	61	PC4
DGND	28		DGND
PC3	29	63	PC2
PC1	30		PC0
PA7	31	65	PA6
		66	PA4
PA3		67	PA2
PA1	34	68	PA0
* Note: Applied output related	nina	on th	- DAO/DAO- 2214

* Note: Analog output related pins on the DAQ/DAQe-2214

NC / DA0OUT*	1	35	AOGND* / NC
NC / DA1OUT*	2	36	AOGND* / NC
NC / AOEXTREF*	3	37	AOGND* / NC
NC	4	38	NC
DGND	5	39	DGND
RESERVED / EXTWFTRIG*	6	40	DGND
EXTDTRIG	7	41	DGND
SSHOUT	8	42	DGND
RESERVED	9	43	DGND
RESERVED	10	44	DGND
RESERVED / AFI1*	11	45	DGND
AFI0	12	46	DGND
GPTC0_SRC	13	47	DGND
GPTC0_GATE	14	48	DGND
GPTC0_UPDOWN	15	49	DGND
GPTC0_OUT		50	DGND
GPTC1_SRC	17		DGND
GPTC1_GATE	18		DGND
GPTC1_UPDOWN	19	53	DGND
GPTC1_OUT	20		DGND
EXTTIMEBASE	21		DGND
PB7	22		PB6
PB5	23		PB4
PB3	24		PB2
PB1	25		PB0
PC7	26		PC6
PC5	27		PC4
DGND		62	DGND
PC3	29	63	PC2
PC1	30	64	PC0
PA7	31	65	PA6
PA5	32		PA4
PA3		67	PA2
PA1	34	68	PA0

Pin Assignment

Connector CN1

AI0	(AIH0)	1	35	(AILO)	AI8
AI1	(AIHI)	2	_	(AILU)	Al9
AI1	(AIH1)	3	36 37	(AIL1)	AI9 AI10
AI2	(AIH2)	4	38	(AIL2)	AI10
	(AIH4)	_	_	(AIL3)	
AI4 AI5	(AIH4)	5 6	39	(AIL4)	AI12 AI13
			40		
AI6	(AIH6)	7	41	(AIL6)	AI14
AI7	(AIH7)	8	42	(AIL7)	AI15
	NC	9	43	NC	
	NC	10	44	NC	
	NC	11	45		
	NC	12	46		
	NC	13	47	NC	
	NC	14	48	NC	
	NC	15	49		
	NC	16	50		
Al	SENSE	17	51	AIGND	
	NC	18	52	NC	
	NC	19	53	NC	
	NC	20	54	NC	
	NC	21	55	NC	
	NC	22	56	NC	
	NC	23	57	NC	
	NC	24	58	NC	
	NC	25	59	NC	
	NC	26	60	NC	
	NC	27	61	NC	
	NC	28	62	NC	
	NC	29	63	NC	
	NC	30	64	NC	
	NC	31	65	NC	
	NC	32	66	NC	
	NC	33	67	NC	

Termination Boards

■ DIN-68S-01

Termination Board with one 68-pin SCSI-II Connector and DIN-Rail Mounting (Cables are not included. For information on mating cables, refer to Section 12.)

SSI Bus Cables

(for multiple cards synchronization)

ACL-SSI-2

SSI Bus cable for 2 devices ■ ACL-SSI-3

SSI Bus cable for 3 devices ACL-SSI-4

SSI Bus cable for 4 devices

Ordering Information

16-CH 16-Bit 250 kS/s Low-Cost Multi-Function DAQ Card w/o Analog Output

■ DAQ-2214 16-CH 16-Bit 250 kS/s Low-Cost Multi-Function DAQ Card with 2-CH Analog Outputs

16-CH 16-Bit 250 kS/s Low-Cost Multi-Function PCI Express® DAQ Card w/o Analog Output

EXTATRIG 34 68 AIGND

■ DAQe-2214

16-CH 16-Bit 250 kS/s Low-Cost Multi-Function
PCI Express® DAQ Card with 2-CH Analog Outputs



SSI bus cable for multiple cards synchronization



Termination board DIN-68S-01 & 68-Pin SCSI-VHDCI cable ACL-10568-1

Quick Selection Guide

Model	Analog Input		Analog Output			DIO	Timer/Counter		
number	No. of channels	Resolution	Sampling rate	Input range	No. of channels	Resolution	Update rate	No. of channels	No. of channels
DAQ-2213/ DAQe-2213	8 DI/16 SE	16 bits	250 kS/s	±1.25 V to ±10 V				24-CH 8255 PIO	2-CH, 16-bit
DAQ-2214/ DAQe-2214	8 DI/16 SE	16 bits	250 kS/s	±1.25 V to ±10 V	2	12 bits	1 MS/s	24-CH 8255 PIO	2-CH, 16-bit

Model Number	DAQ-2213/DAQe-2213	DAQ-2214/DAQe-2214				
Analog Input						
Resolution	16 bits, no m	issing codes				
Number of channels	16 single-ended or 8 differential (software selectable per channel)					
Channel gain queue size	512					
Maximum sampling rate	250					
Programmable gain	1, 2,	*				
Bipolar input ranges	±10 V, ±5 V, ±	· · · · · · · · · · · · · · · · · · ·				
Unipolar input ranges	0-10 V, 0-5 V, 0-2.5 V, 0-1.25 V					
Offset error		mV				
Gain error	±0.01%					
Input coupling		C				
Overvoltage protection	Power on: Continuous ±30 V,					
Input impedance	1 GΩ /	•				
CMRR (gain = 1)	83					
Settling time	4 µs to 0.1	kHz				
-3 dB small signal bandwidth (gain = 1)	Software, external digita					
Trigger sources	Pre-trigger, post-trigger, middle-trigge					
Trigger modes FIFO buffer size	Pre-trigger, post-trigger, middle-trigger					
Data transfers	Polling, scatte	In the second se				
Analog Output	1 Oming, scatte	a-gattlet DWA				
Number of channels		2 voltage outputs				
Resolution		12 bits				
Output ranges		0-10 V, ±10 V, 0-AOEXTREF, ±AOEXTREF				
Maximum update rate		1 μs				
Slew rate		20 V / μs				
Settling time		3 μs to ±0.5 LSB accuracy				
Offset error		±1 mV				
Gain error		±0.02 % of max. output				
Driving capacity		±5 mA				
Stability		Any passive load, up to 1500 pF				
Trigger sources		Software, external digital/analog trigger, SSI bus				
Trigger modes		Post-trigger, delay-trigger, and repeated trigger				
FIFO buffer size		1 k samples				
Data transfers		Programmed I/O, scatter-gather DMA				
Digital I/O						
Number of channels	24-CH 8255 prograr	mmable input/output				
Compatibility	5 V/TTL					
Data transfers	Programmed I/O					
General-Purpose Timer/Counter						
Number of channels	2	2				
Resolution	16 bits					
Compatibility	5 V/TTL					
Base clock available	40 MHz, external o	clock up to 10 MHz				
Auto Calibration						
Onboard reference	+5 V					
Temperature drift	±2 ppm/°C ±6 ppm/1000 Hrs					
Stability Company Specifications	±6 ppm/	1000 Hrs				
General Specifications	475 (G 000000toro) /DAO 2242/2244)				
Dimensions	175 mm x 107 mm (not including connectors) (DAQ-2213/2214)					
Connector	168 mm x 107 mm (not including connectors) (DAQe-2213/2214)					
Connector Operating temperature	68-pin VHDCI female x 2					
Operating temperature	0 to 55°C					
Storage temperature Relative Humidity	-20 to 70°C 5 to 95 %, non-condensing					
Power requirements	·					
I OWEL IEUUII EI II EI II E	+5 V 1.2 A typical (DAQ-2213)	+5 V 1.2 A typical (DAQ-2214)				

